

**INT-9951 PCI ITU / CEPT E1 Demultiplexer  
Specifications**

**User Signal Interface Connector**

Number of Pins	80
PCB Connector	AMP P/N 787190-8
PCB Mating Connector	AMP P/N 749111-7
Straight Shielded Backshell	AMP P/N 749196-1
Right Angle Backshell	AMP P/N 749205-1
Pin Assignments	See Appendix A

**User Input Signal Definitions**

Signal Name	E1_IN_TIP, E1_IN_RING
Rate	2048 Kbps
Format	ITU G.703, G.704
Coding	AMI/HDB3
Impedance	120 Ohms Balanced Differential

Signal Name	ECL_IN_DATA, ECL_IN_DATA/ ECL_IN_CLOCK, ECL_IN_CLOCK/
Format	MECL 10KH Logic Levels
Coding	NRZ
Impedance	100 Ohms Balanced Differential Each Signal
Clock	Rising Edge Centered on Data Bit

Signal Name	LVDS_IN_DATA, LVDS_IN_DATA/ LVDS_IN_CLOCK, LVDS_IN_CLOCK/
Format	IEEE 1394
Coding	NRZ
Impedance	100 Ohms Balanced Differential Each Signal
Clock	Rising Edge Centered on Data Bit

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**User Output Signal Definitions**

Signal Name	E1_OUT1_TIP, E1_OUT1_RING
Rate	2048 Kbps
Format	ITU G.703, G.704
Coding	AMI/HDB3
Impedance	120 Ohms Balanced Differential

Signal Name	E1_OUT2_TIP, E1_OUT2_RING
Rate	2048 Kbps
Format	ITU G.703, G.704
Coding	AMI/HDB3
Impedance	120 Ohms Balanced Differential

Signal Name	TTL_OUT_DATA, TTL_OUT_DATA/ TTL_OUT_CLOCK, TTL_OUT_CLOCK/
Format	TTL Logic Levels
Coding	NRZ
Impedance	Un-terminated, MC74HCT241 TTL bus driver
Clock	Rising Edge Centered on Data Bit

Signal Name	AUDIO_OUT_1 through AUDIO_OUT_31
Rate	300 Hz – 3400 Hz
Format	Analog, +/- 2.5V maximum
Impedance	10K Ohms Un-Balanced Single Ended
Source	Selectable from any demultiplexed E1 timeslot

**PCI Interface**

Specifications	PCI Local Bus Specification, Revision 2.2
Size	Full length, single slot
Slot Type	+5V or +3.3V
Bus Clock	33 MHz maximum
Software Supported	WinNT, Win/2000, Solaris (SPARC), Solaris 8 (Intel), Linux, Irix

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**Physical**

Size	Full length PCI card, (12.25" x 4.20" x 0.60")
PCI Slots Required	One
Weight	12 Oz
Power	7 Watts (nominal) (1.5A @ +5VDC)
Operating Temp	0 – 50 degrees C (Non-Condensing)
Storage Temp	-20 – 60 degrees C (Non-Condensing)

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**Edge Connector Pin Assignments**

AMP 749111-7	STANDARD RIBBON P1	SIGNAL		AMP 749111-7	STANDARD RIBBON P2	SIGNAL
1	1	AUDIO_OUT_1		41	1	AUDIO_OUT_17
2	2	GND		42	2	GND
3	3	AUDIO_OUT_2		43	3	AUDIO_OUT_18
4	4	GND		44	4	GND
5	5	AUDIO_OUT_3		45	5	AUDIO_OUT_19
6	6	GND		46	6	GND
7	7	AUDIO_OUT_4		47	7	AUDIO_OUT_20
8	8	GND		48	8	GND
9	9	AUDIO_OUT_5		49	9	AUDIO_OUT_21
10	10	GND		50	10	GND
11	11	AUDIO_OUT_6		51	11	AUDIO_OUT_22
12	12	GND		52	12	GND
13	13	AUDIO_OUT_7		53	13	AUDIO_OUT_23
14	14	GND		54	14	GND
15	15	AUDIO_OUT_8		55	15	AUDIO_OUT_24
16	16	GND		56	16	GND
17	17	AUDIO_OUT_9		57	17	AUDIO_OUT_25
18	18	GND		58	18	GND
19	19	AUDIO_OUT_10		59	19	AUDIO_OUT_26
20	20	GND		60	20	GND
21	21	AUDIO_OUT_11		61	21	AUDIO_OUT_27
22	22	GND		62	22	GND
23	23	AUDIO_OUT_12		63	23	AUDIO_OUT_28
24	24	GND		64	24	GND
25	25	AUDIO_OUT_13		65	25	AUDIO_OUT_29
26	26	GND		66	26	GND
27	27	AUDIO_OUT_14		67	27	AUDIO_OUT_30
28	28	GND		68	28	GND
29	29	AUDIO_OUT_15		69	29	AUDIO_OUT_31
30	30	GND		70	30	GND
31	31	AUDIO_OUT_16		71	31	GND
32	32	GND		72	32	ECL_IN_DATA/
33	33	LVDS_IN_DATA/		73	33	ECL_IN_DATA
34	34	LVDS_IN_DATA		74	34	ECL_IN_CLOCK/
35	35	LVDS_IN_CLOCK/		75	35	ECL_IN_CLOCK
36	36	LVDS_IN_CLOCK		76	36	E1_OUT2_RING
37	37	TTL_OUT_CLOCK		77	37	E1_OUT2_TIP
38	38	TTL_OUT_DATA		78	38	GND
39	39	E1_OUT1_RING		79	39	E1_IN_RING
40	40	E1_OUT1_TIP		80	40	E1_IN_TIP